Shankar Krishnamoorthy

Head of Technology & Product Development Group

Shankar Krishnamoorthy leads the Technology & Product Development Group (TPG), which is responsible for researching, creating, and delivering innovative, scalable, high-quality products and platforms for Synopsys across design, verification, signoff, test, silicon lifecycle management (SLM), TCAD, manufacturing domains for digital, analog, and mixed-signal designs, hardware-assisted verification, and systems software.

Prior to leading TPG, Shankar held other executive positions at Synopsys, including GM of the Electronic Design Automation Group and Digital Design Group and Senior Vice President of the Digital Implementation Group. Under his leadership, Synopsys has delivered several ground-breaking technologies enabling semiconductor designers to achieve the most optimum power, performance, area, cost, and time to market on their most advanced products. Before rejoining Synopsys in 2017, Shankar served as GM of the IC Design Solutions Division at Mentor Graphics. He joined Mentor in 2007 with the acquisition of Sierra Design Automation, where he was founder and CTO. Prior to Sierra Design, Shankar led Synopsys' physical synthesis and logic synthesis R&D organizations.

Shankar received his M.S. in Computer Science from the University of Texas, Austin, and his bachelor's degree in Computer Science from the Indian Institute of Technology, Bombay.